

F I G. 1

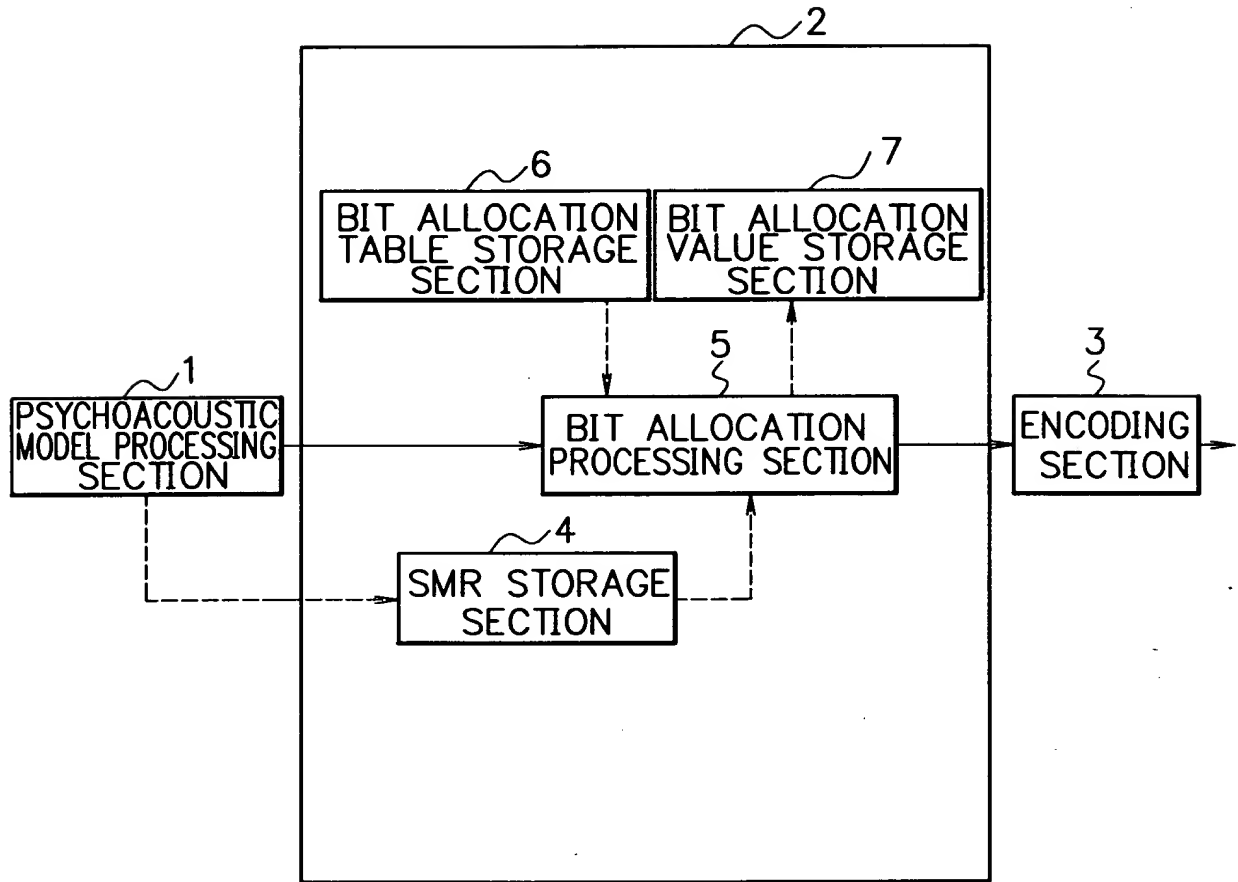


FIG. 2

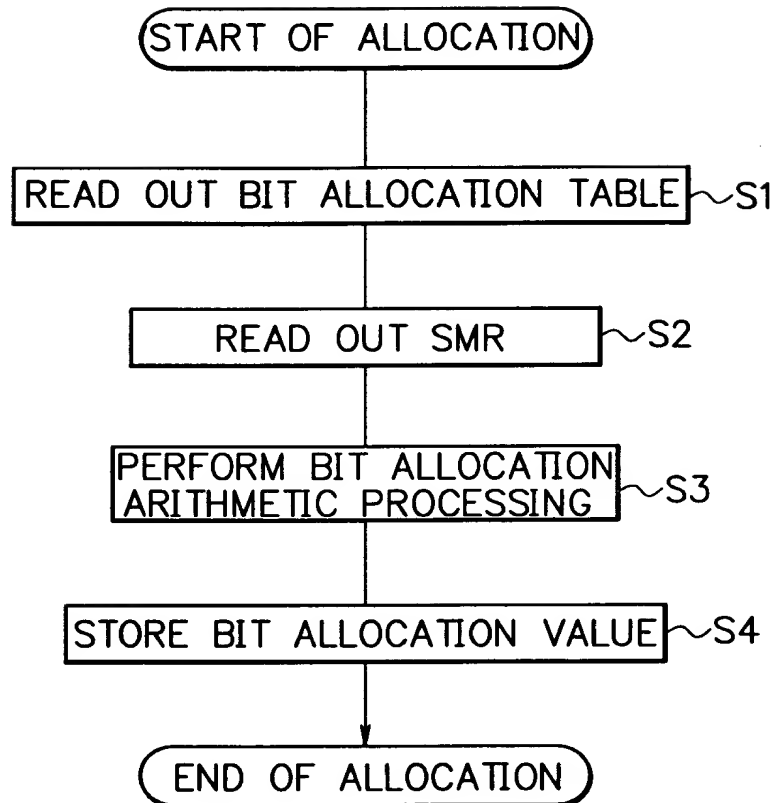


FIG. 3

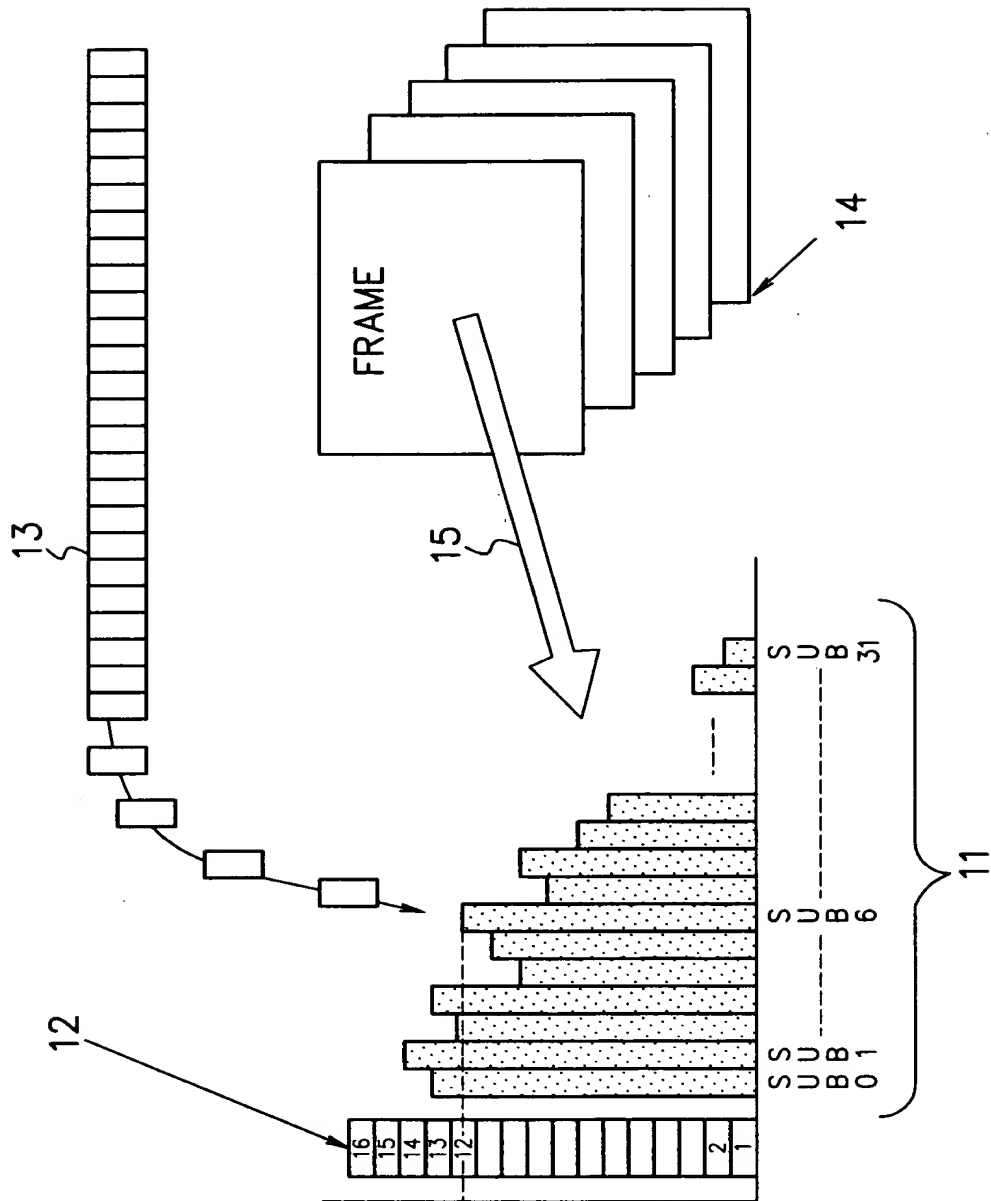


FIG. 4

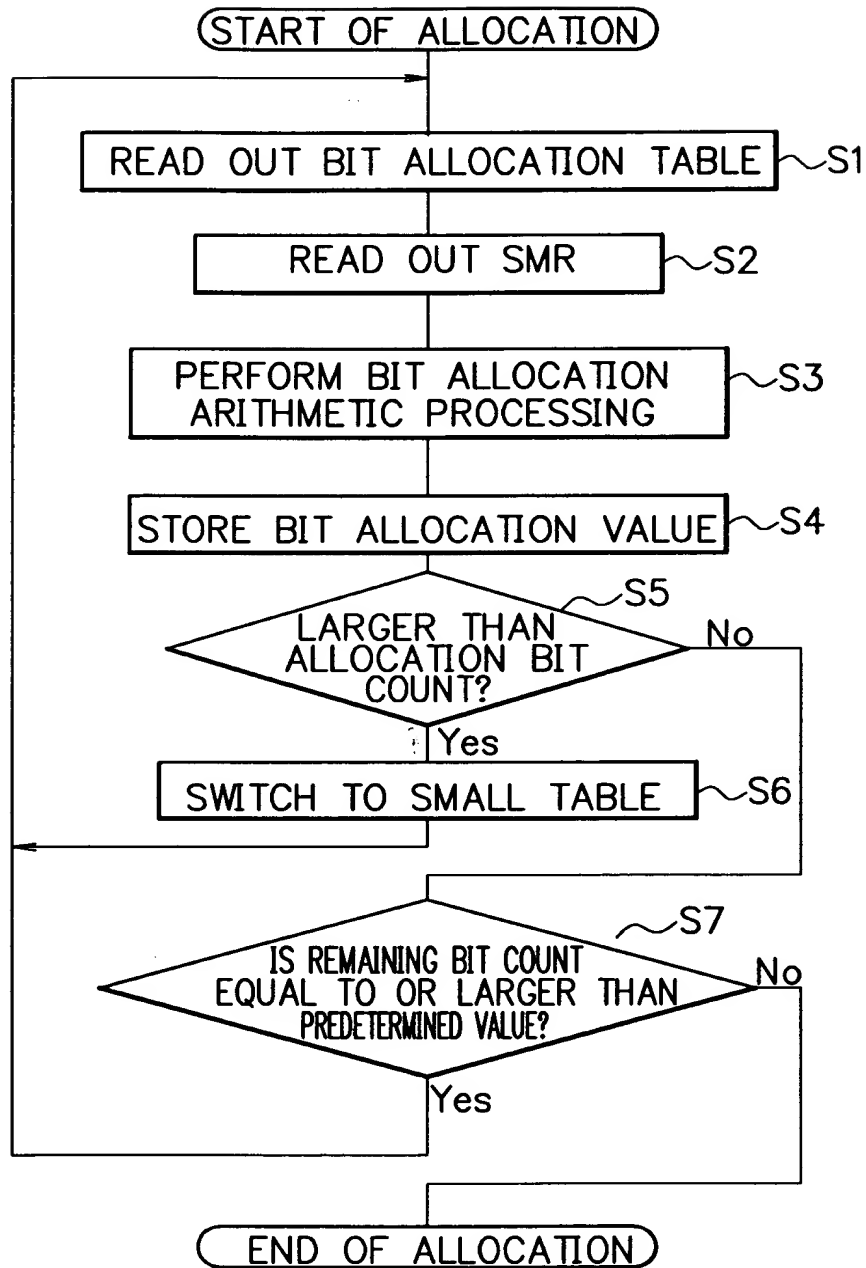


FIG. 5

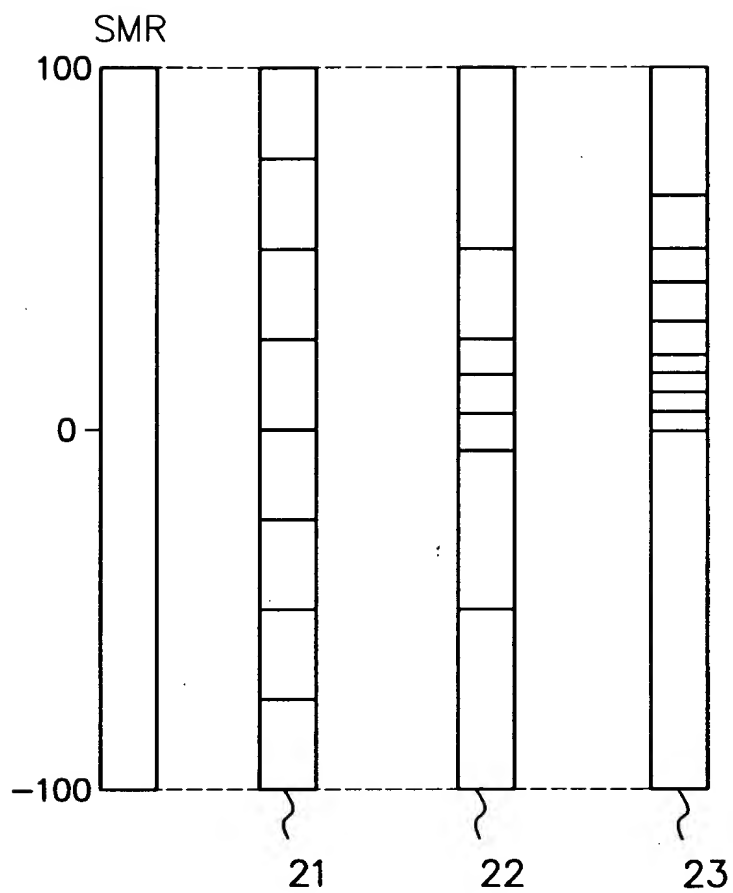


FIG. 5

FIG. 6

BIT ALLOCATION COUNT j	CORRESPONDING SMR LEVEL
16	80.00
15	74.00
14	68.00
13	62.00
12	56.00
11	50.00
10	44.00
9	38.00
8	32.00
7	26.00
6	21.00
5	16.00
4	11.00
3	7.00
2	0.00
1	-20.00
0	-

FIG. 7

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
22.6	16.4	11.5	10.7	6.3	3.4	2.8	3.8	4.9	2.6	1.5	0.9	-1.2	-1.8	-5.1	-6.6
16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
-4.0	-7.9	-8.0	-9.8	-12.1	-15.3	-26.4	-31.1	-34.2	-37.9	-44.8	-47.3	-56.0	-61.5	-80.9	-78.1

FIG. 8

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
6	5	4	3	2	2	2	2	2	2	2	2	1	1	1	1
16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0

F I G. 11

BIT ALLOCATION COUNT j	CORRESPONDING SMR LEVEL
16	74.00
15	68.00
14	62.00
13	56.00
12	50.00
11	44.00
10	38.00
9	32.00
8	26.00
7	21.00
6	16.00
5	11.00
4	7.00
3	0.00
2	-20.00
1	-50.00
0	-

F I G. 12

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
7	6	5	4	3	3	3	3	3	3	3	3	2	2	2	2
16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
2	2	2	2	2	2	1	1	1	1	1	1	0	0	0	0

05704193 "024604

FIG. 13

PRIOR ART

